



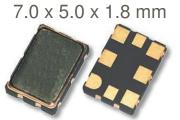


SX7LF

LVDS SURFACE MOUNT CRYSTAL CLOCK OSCILLATOR

FEATURES

- 4 Frequencies Switchable Oscillator
- Ultra-low Jitter
- Up to 2100 MHz
- Short delivery



Item	Specification								
Frequency Range	15.0 MHZ ~ 2100 MHz								
Output Signal	LVDS								
Overall Frequency Stability*	± 25 ppm ~ ± 100 ppm (see options)								
Operating Temperature Range	$0 \sim +70 ^{\circ}$ C commercial application (see options) -40 $\sim +85 ^{\circ}$ C industrial application (see options)								
Supply Voltage Vdd	+2.5V ±10%		+3.3V ±10%						
Supply Current Idd	75 mA typ. ; 90 mA max.								
Output Voltage HIGH VOH	1.43 V typ. ; 1.6 V max.								
Output Voltage LOW VOL	1.10 V typ.; 0.9 V min.								
Output Load	50 ohm from each output								
Symmetry	45/ 55%								
Rise Time/Fall Time Fr/Ff	0.35 ns max.								
Start-up Time	3 ms typ.; 10 ms max.								
RMS Phase Jitter (12 kHz to 20 MHz)	150 fs typ.								
Frequency selection configurations	FS1 0 0 1 1 1 1	FS0 0 1 0 1	Frequency output Freq. 1 Freq. 2 Freq. 3 Freq. 4						
Frequency Select Timing	2.5 ms max.								
Packing Unit	1000pcs / reel								
Soldering Condition	260°C, 10 sec x2 max								
	Customer specifications on request								

^(*) Includes initial tolerance@+25°C, stability over operating temperature, stability vs. load change, stability vs. supply change and one year aging

OPTIONS & ORDERING INFORMATION

SX7LF				Frequency 1 (MHz)	Frequency 2 (MHz)	Frequency 3 (MHz)	Frequency 4 (MHz)
Supply Voltage*	Operating Temp. *	Overall Stability *	Tri-state Function				
25 = +2.5V	$E = 0^{\circ}/+70^{\circ}C$	20 = ±20 ppm	F = No Tri-state				
33 = +3.3V	F = -20°/+70°C	25 = ±25 ppm					
	$K = -40^{\circ}/+85^{\circ}C$	30 = ±30 ppm					
		50 = ±50 ppm					
		100 = ± 100ppm					

^{*} Note : Not all combinations are possible, please consult us.



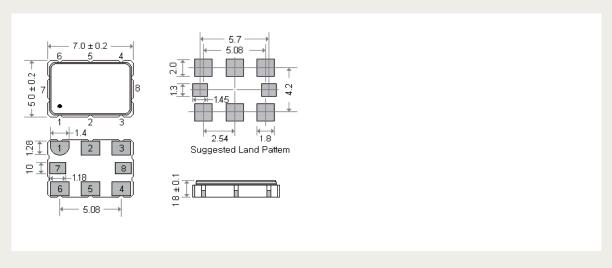








OUTLINE DIMENSIONS (mm)



Pin Connections

#1 : FS0 Control

#4 : Output

#7 : Do not connect

#2: FS1 Control

#5 : Complementary output

#8 : Do not connect

#3: GND

#6: Vdd





